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Examiner: David A. Zarneke

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FAN OUT TYPE WAFER LEVEL PACKAGE STRUCTURE AND METHOD OF THE SAME

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Other Documents (Inc. Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initials	Document	
	Yuan et al., "Design, Experiment and Analysis of the Solder on Rubber (SOR) Structure of WLCSP, 7 th Int. Conf. on Thermal, Mechanical and Multiphysics Simulation and Experiments in Micro-Electronics and Micro-Systems, EuroSimE 2006.	
Examiner:		Date Considered:
/D	David Zarneke/	06/07/2008

**Examiner:

Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.